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How can I set minimum
number of steps between to
be calculated when using
matplotlib animation? In the
following example I would like
to animate a parameter
moving from a value of 1 to a
value of 2, but it takes one
step before the value
changes. Instead, I would like
to set a minimum number of
steps between to be
calculated when using
matplotlib animation. import
matplotlib.pyplot as plt
import matplotlib.animation

```
as animation def
animate(num_frames): fig =
plt.figure() ax = plt.gca() def
animate_line(frame_number):
x = ((frame_number - 1) /
10.) * 100 ax.plot(x, [1])
plt.draw() ani =
animation.FuncAnimation(fig,
animate_line,
interval=num_frames,
```

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Field of the Invention The
present invention generally
relates to system bus
interfaces and methods, and,
more particularly, to a system
bus interface and method
which allows for multiple

buses to access a single memory, and which can be used to bridge between an AGP (Accelerated Graphics Port) and a PCI (Peripheral Component Interconnect) bus.

2. Description of the Related Art

In conventional bus interface systems, a bus can be accessed only by one bus master. Some conventional bus interfaces may allow multiple buses to access the same memory, but all of them generally require a single bus master such as the CPU. Furthermore, the access between the memory and the bus is generally controlled by the bus master, i.e., the bus master determines whether to read data from the memory and write data to the memory. For example, the CPU determines whether to read a data cache line or to write a data cache

line for an AGP access, and the CPU also determines whether to read a data cache line or to write a data cache line for a PCI access, etc. In other words, there is only one bus master and the access is controlled by the bus master. However, different masters using different bus interface standards may wish to access the same memory. For example, the AGP standard may have a custom bus interface without a CPU-based bus master (xe2x80x9cAGP with no CPU bus masterxe2x80x9d), or the AGP standard may have a CPU-based bus master with a CPU-based system clock (xe2x80x9cCPU-based AGP bus masterxe2x80x9d). In addition, there may be more than one bus master. For example